

## ABSTRACT

The known method for manufacturing electronic components uses solder for mounting an electronic component on a ceramic substrate. Consequently, the height of the ceramic substrate including the electronic component is increased by the amount of the solder applied. This is disadvantageous in achieving a more low-profile electronic component. Another approach may be proposed in which the electronic component is embedded in the ceramic substrate to reduce the profile. This approach however requires that a cavity be formed in the ceramic substrate.

A chip-mounted substrate 10 of the present invention includes a chip electronic component 12 on a ceramic substrate 11 having surface electrodes 11C. The chip electronic component 12 includes a ceramic sintered compact as an element assembly and terminal electrodes 12D and 12E. The surface electrodes 11C of the ceramic substrate 11 are integrated with the corresponding external terminal electrodes 12D and 12E by sintering.